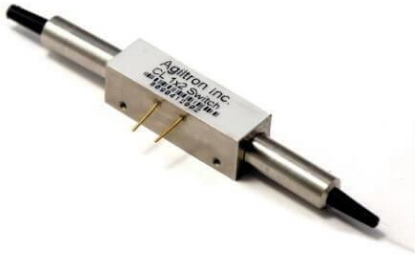


CrystaLatch™ Fiber Optic Polarization Switch



DATASHEET

BUY NOW



Features

- No moving part
- Fast switching
- Low IL
- Miniature Size

Applications

- Test instrument
- Sensor

The CL Series polarization switch can quickly switch the incoming SOP between two orthogonal polarization states (SOPs). This is achieved using patented non-mechanical configurations and activated via an electrical control signal. Latching operation preserves the selected SOP after the drive signal has been removed. The all solid state CL polarization fiber optic switch features low insertion loss, high extinction ratio, fast switching, and extremely high reliability and repeatability. The input is PM fiber.

The output could be either PM or SM fiber. For PM fiber, the polarization is aligned with slow axis. Electronic driver is available for this series of switches.

The magneto-optical crystals used in the CL switches have no fatigue nor drift effect.

Specifications

Parameter	Min	Typical	Max	Unit
Operation Wavelength ^[1]	1520	1550	1580	nm
	1295	1310	1325	
Insertion Loss ^[2]		0.7	1.0 (1.2 ^[4])	dB
SOP Tolerance		± 1.0 ^[1]	± 2.5 ^[2]	Degree
Return Loss	50	55		dB
PDL (SM Series)		0.1	0.2	dB
Extinction Ratio	18	25		dB
Optical Response (rise, fall)	5		10	µs
Repetition Rate		2K		Hz
Polarization Mode Dispersion		0.1	0.2	ps
Operating Temperature	-5		70	°C
Storage Temperature	-40		85	°C
Optical Power Handling ^[3]		300	500	mW
			2	W
Durability	10 ¹⁵			cycles

Notes:

[1]. Measured at room temperature

[2]. It may be increased at temperature other than room temperature

Legal notices: All product information is believed to be accurate and is subject to change without notice. Information contained herein shall legally bind Agiltron only if it is specifically incorporated into the terms and conditions of a sales agreement. Some specific combinations of options may not be available. The user assumes all risks and liability whatsoever in connection with the use of a product or its application.

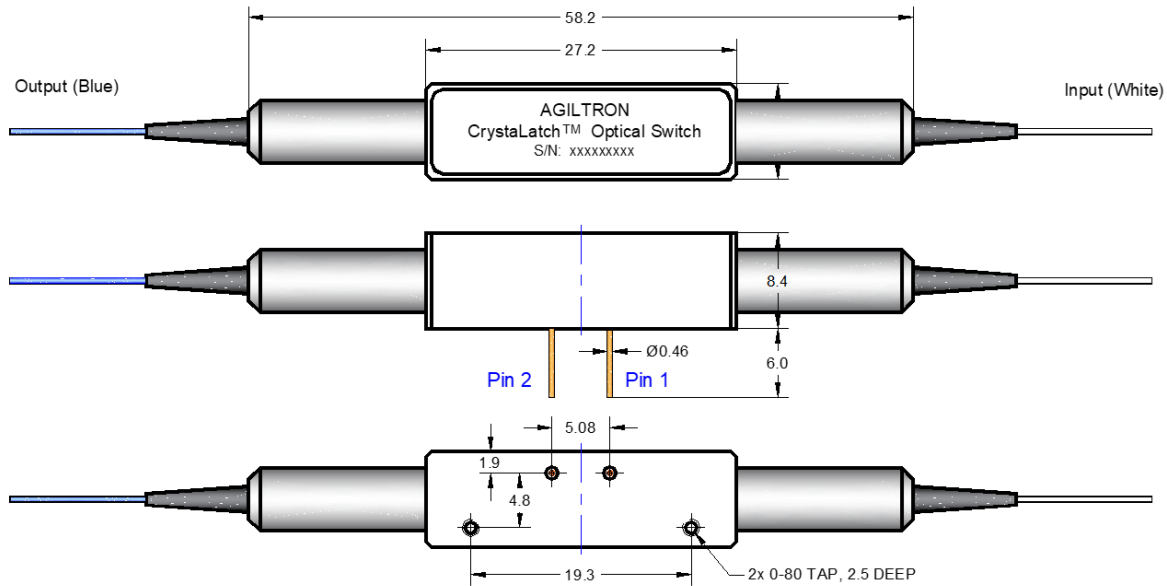
Rev 03/14/24

CrystaLatch™ Fiber Optic Polarization Switch



DATASHEET

Mechanical Dimensions (Unit: mm)



*Product dimensions may change without notice. This is sometimes required for non-standard specifications.

Electrical Driving Information

The polarization switch is actuated by applying a voltage pulse. Applying one polarity pulse, The output light beam polarization is one status. Applying a reversed polarity pulse, The output light beam polarization is rotated by 90 degrees. The status is kept until the next pulse.

Parameter	Minimum	Typical	Maximum	Unit
Drive Voltage ^[1]	2.3	2.5	2.7 ^[1]	V
Drive Current	110	140	195	mA
Pulse Duration	0.2	0.3	0.5	ms

[1]. Over this value will damage the device

Note:

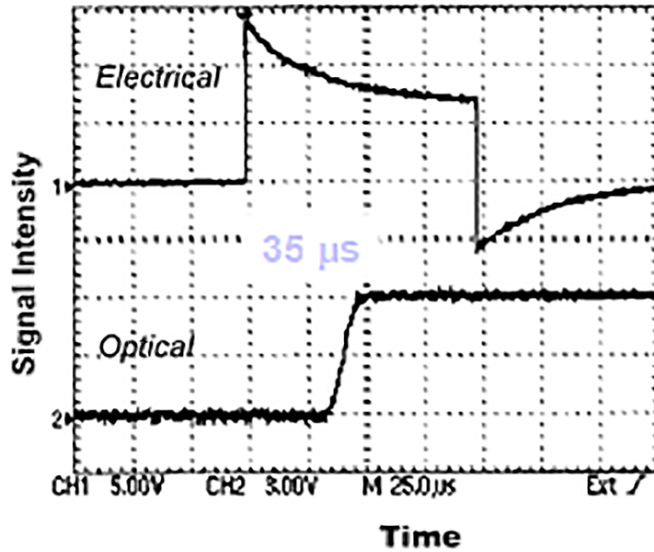
The driving voltage value is transient voltage with a full load. The driver circuitry needs to provide sufficient current (~300mA) during the switching. Inside the switch core is an electromagnet with a residual magnetic field. The residual magnetic field will be established when an electrical current flows in one direction through the coil for a sufficiently long period. The residual magnetic field latches the switch state even without applying a voltage (the current flow stopped). Flowing a current in the opposite direction for a sufficient time changes the switch stage by establishing a reversal magnetic field. The coil is forgiving to the driver unless one burns it by applying a higher voltage or a current for too long (day). The switch can also be operated at high repetition rates of kHz, where the residual magnetic field may not be fully established.

CrystaLatch™ Fiber Optic Polarization Switch

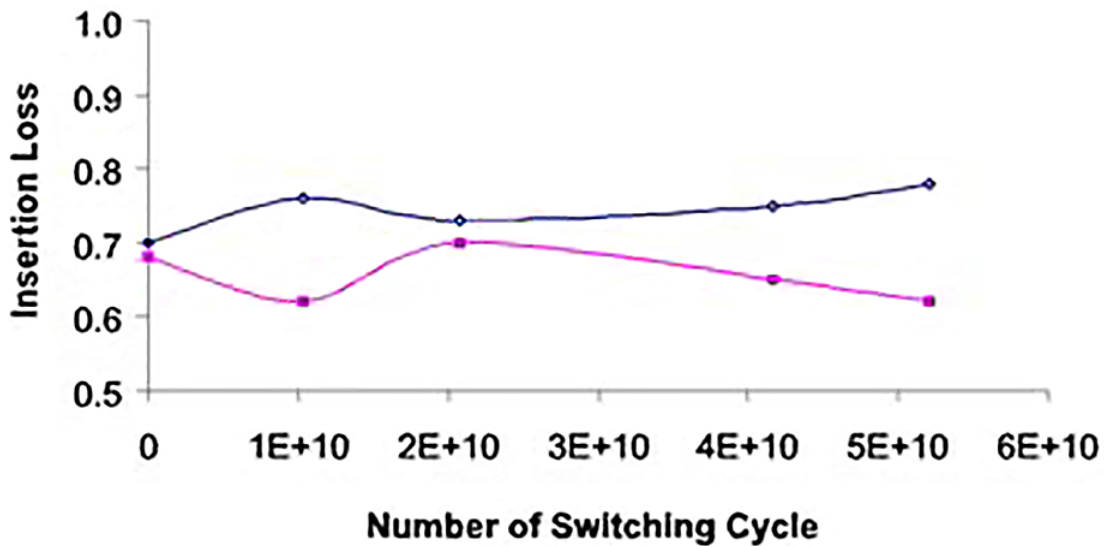


DATASHEET

Typical Switching Response



Typical Loss Change of 1x2 vs Switching Numbers



CrystaLatch™ Fiber Optic Polarization Switch



DATASHEET

Ordering Information

Prefix	Type	Configuration	Package	Wavelength	Input Fiber	Output Fiber	Fiber Length	Connector ^[9]
CLPS-	1 1	1	<input type="checkbox"/> 900 μm tube = 1 <input type="checkbox"/> Bare fiber = 3	<input type="checkbox"/> 1550 nm = 1 <input type="checkbox"/> 1310 nm = 3	<input type="checkbox"/> PM 1550 = 1 <input type="checkbox"/> Special = 0	<input type="checkbox"/> PM 1550 = 1 <input type="checkbox"/> SMF-28 = 2 <input type="checkbox"/> Special = 0	<input type="checkbox"/> 0.25m = 1 <input type="checkbox"/> 0.5m = 2 <input type="checkbox"/> 1.0m = 3 <input type="checkbox"/> Special = 0	<input type="checkbox"/> None = 1 <input type="checkbox"/> FC/PC = 2 <input type="checkbox"/> FC/APC = 3 <input type="checkbox"/> SC/PC = 4 <input type="checkbox"/> SC/APC = 5 <input type="checkbox"/> ST/PC = 6 <input type="checkbox"/> LC/PC = 7 <input type="checkbox"/> Duplex LC/PC = 8 <input type="checkbox"/> LC/APC = A <input type="checkbox"/> LC/UPC = U <input type="checkbox"/> Special = 0

Fiber Core Alignment

Note that the minimum attenuation for these devices depends on excellent core-to-core alignment when the connectors are mated. This is crucial for shorter wavelengths with smaller fiber core diameters that can increase the loss of many decibels above the specification if they are not perfectly aligned. Different vendors' connectors may not mate well with each other, especially for angled APC.

Fiber Cleanliness

Fibers with smaller core diameters (<5 μm) must be kept extremely clean, contamination at fiber-fiber interfaces, combined with the high optical power density, can lead to significant optical damage. This type of damage usually requires re-polishing or replacement of the connector.

Maximum Optical Input Power

Due to their small fiber core diameters for short wavelength and high photon energies, the damage thresholds for device is substantially reduced than the common 1550nm fiber. To avoid damage to the exposed fiber end faces and internal components, the optical input power should never exceed 20 mW for wavelengths shorter 650nm. We produce a special version to increase the handling by expanding the core side at the fiber ends.

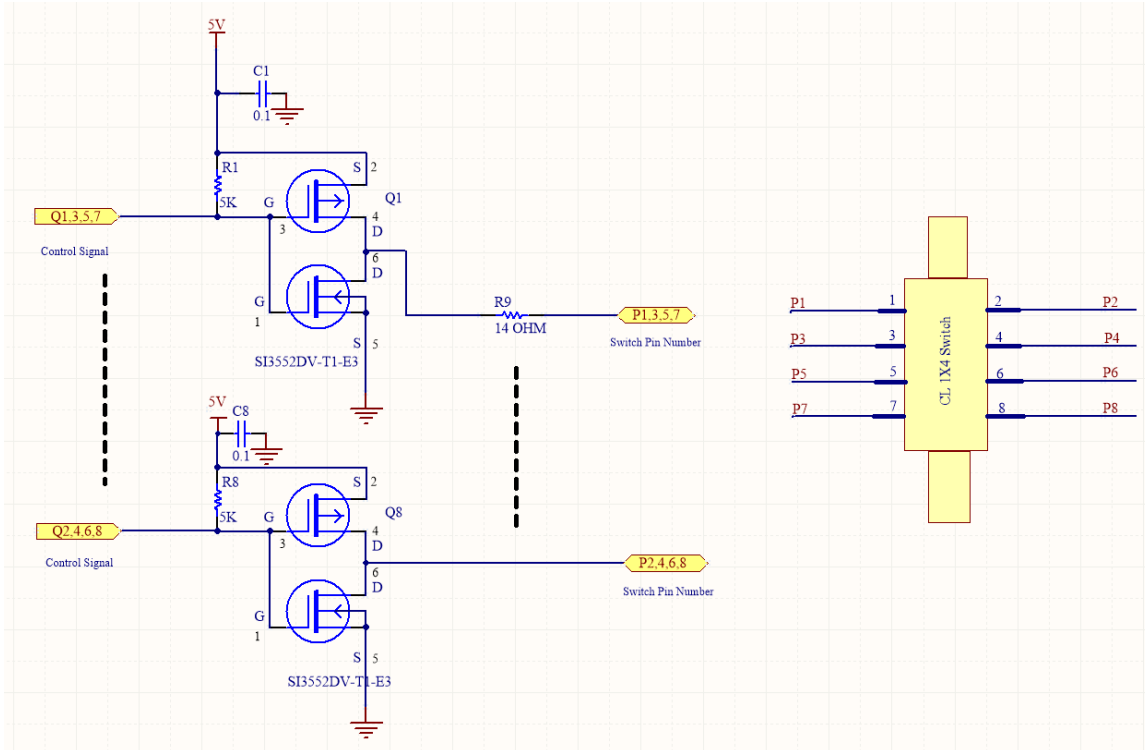
CrystaLatch™ Fiber Optic Polarization Switch



DATASHEET

Driver Design Example for 1x4

A recommended +5VDC powered driving circuit is provided. The resistor network R1~R8 is to suppress the driving signal's voltage level to meet the "switch voltage" requirements. In specific applications, users can use lower voltage to eliminate the R1~R8. The Q1~Q8 is the control signal from either a function generator or a microcontroller general purpose I/O. The Q1-Q8 switching speed must meet the specific MOSFET switching requirement and CL 1x4 Switch specific requirement. Usually, the control signal speed is $\leq 2\text{kHz}$.



Usually, a clean power supply source will be sufficient. However, decoupling capacitors for the transistor supply rail are recommended depending on different applications. Minimum the current loop on the switching circuits will minimize the switching noise. For other layout recommendations, please refer to books or application notes from the IC manufacturer.